



The 9th International Symposium on Applied Reconfigurable Computing

ARC 2013

25-27 March, 2013, Los Angeles, California, USA
Web: <http://www.isi.edu/events/arc2013/>

Call for Papers

The ARC Symposium

Reconfigurable computing technologies offer the promise of substantial performance gains over traditional architectures via the customizing, even at runtime, the topology of the underlying architecture to match the specific needs of a given application

Contemporary configurable architectures allow for the definition of architectures with functional and storage units that match in function, bit-width and control structures the specific needs of a given computation. The flexibility enabled by reconfiguration is also seen as a basic technique for overcoming transient failures in emerging device structures.

ARC aims at bringing together researchers and practitioners of reconfigurable computing with an emphasis on practical applications of this promising technology. This year's workshop will have a series of international invited speakers that will express their view on the future of reconfigurable technology

The ARC 2013 proceedings will be published by Springer-Verlag as a Lecture Notes in Computer Science series volume and will also be available through the SpringerLink on-line service (www.springer.com/lncs). Additionally, selected submissions will be invited for consideration for the ACM Transactions on Reconfigurable Technology and Systems (TRETS).

Submission Information

Authors are invited to submit original contributions in English including, but not limited to, the areas of interest mentioned below. Submission must be uploaded to the ARC website and identify the format of the contribution as either:

Long Papers: (12 pages maximum) which should include mainly accomplished results (oral presentation)

Short Papers: (6 pages maximum, or 8 pages max. upon payment of additional charge of \$100 USD per page) to be composed of work in progress or reporting recent developments (poster presentation)

The format of the paper should be according to the Springer-Verlag LNCS Series (Lecture Notes on Computer Science) format rules as described in <http://www.springer.de/comp/lncs/author.html>

Important Dates

Submission Deadline

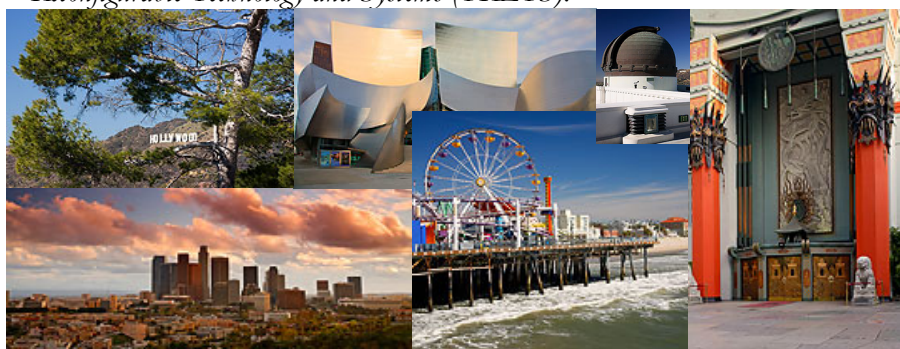
November 9th, 2012 (Extended)

Author Notification Deadline

December 1st, 2012

Camera-ready Author Registrations

December 21st, 2012



Topic of Interest

RC Design Methods & Tools

- High-level Compilation
- Simulation
- Estimation Techniques
- Design-Space Exploration
- Programming Languages and paradigms
- Run-Time Systems
- Compilation and Synthesis

RC Architectures

- Fine-grained and Mixed-grained
- Multi-Processor based
- Embedded Multi-Core
- Reconfigurable Fabric
- Resilient through Reconfiguration
- System Architectures and Integration

RC Applications

- High-Performance Systems
- Embedded Computing
- Robotics
- Digital Signal Processing
- Safety and Mission Critical Systems

RC Critical Issues

- Teaching Reconfigurable Computing
- Surveys and Future Trends
- Benchmarks

General Chair:

Pedro C. Diniz, USC Information Sciences Institute

Technical Program Co-Chairs:

Philip Brisk Univ. California, Riverside, Calif., USA

José G. Coutinho, Imperial College, London, UK